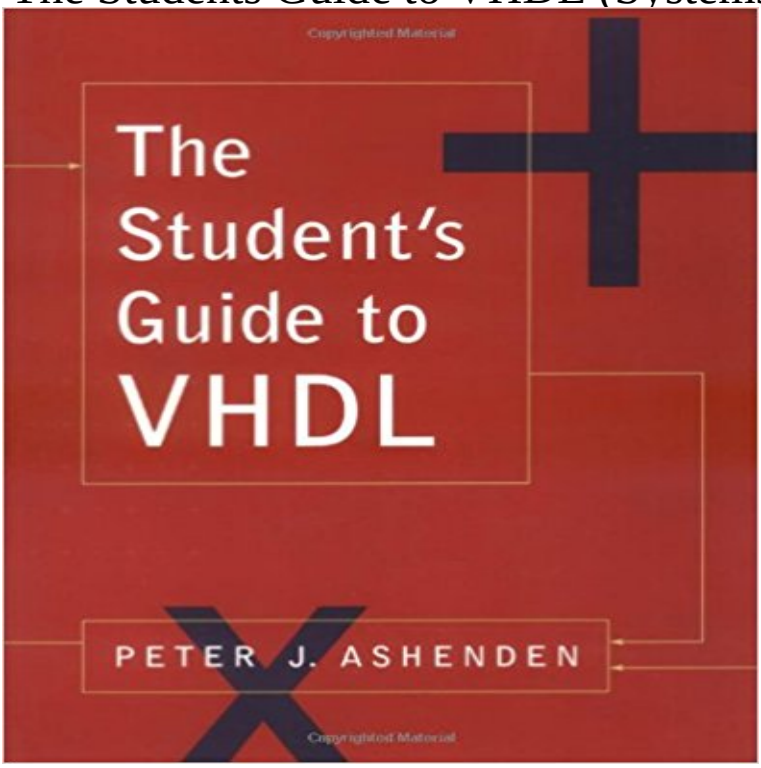
The Students Guide to VHDL (Systems on Silicon)



VHDL is a language for describing digital electronic systems. A vital, efficient step in the system design process, VHDL allows for the design and simulation of a hardware system prior to it actually being manufactured. This new book provides a tutorialintroduction to the fundamental modeling features of VHDL and shows how the features are used for the design of digital systems. Offering the same clear, accessible style as The Designers Guide to VHDL, The Students Guide is designed as a main text for introductory VHDL courses, and as a supplementary text for courses that require VHDL-based project work, such as computer architecture, digital design, and digital logic courses. This new condensed text also serves as a quick, self-teaching guide for practicing engineers who need to learn only the basics of VHDL. * On-line resources include code for case studies. * Numerous exercises that are coded for difficulty and expected solution time. * Solutions to the level one exercises are included in the book for self-testing.* An appendix citing the differences between the 1987 standard and the 1993 standard which is described in the book.

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